

LESSON PLAN YEAR WISE PART-I,II,II

Lesson Plan of Part-I (Hons)						
Subject	Paper	Topic	Sub-topic	Faculty	No. of Class	
CMSA	I	Computer Fundamentals	Introduction to Computer and Problem Solving	Himadri Bhattacharyya	8	
			Hardware			
			Software			
			Generation of Computers			
			Introduction to Programming Languages			
			Problem Solving			
			Number Systems and Codes	Dhiman Karmakar	12	
			Boolean Algebra	Himadri Bhattacharyya	10	
			Introduction to Basic Electronics	Elementary circuit theory	Dyuti Chatterjee	2
				Elementary Physics of semi-conductors		7
	Bipolar Junction Transistor			8		
	Inverters using Transistors			2		
	Unipolar Junction Transistor			8		
	Operational Amplifier			8		
	Digital System Design	Combinational Circuits		Himadri Bhattacharyya	16	
		Sequential Circuits			17	
		Data Converter			12	
	Computer Organization – I	Basic Computer Organization		Himadri Bhattacharyya	10	
		Instruction		Sudeshna Tripathy	15	
		Memory		Himadri Bhattacharyya	15	
	II	OS & System software		System Software Fundamentals	Dhiman Karmakar	10
			Introduction to Operating Systems		4	
			Concepts of Synchronization		7	
Processor Management				4		
I/O Management				3		
Memory Management				7		
File Systems				4		
Dead Lock				7		
Case Study				4		
Data Structure-I			Introduction	Tohida Rehman	2	
			Arrays		5	
			Linked Structures		6	
			Stacks and Queues		10	
	Recursion		2			

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Lesson Plan of Part-II (Hons.)

Subject	Paper	Topic	Sub-topic	Faculty	No. of Class	
CMSA	III	Discrete Mathematical Structures	<i>Graphs</i>	Tohida Rehman	24	
			<i>Mathematical Logic</i>	Dhiman Karmakar	7	
			<i>Algorithms</i>	Dhiman Karmakar	6	
			<i>Counting theory</i>	Barun Dash	18	
			<i>Introduction to Probability</i>	Barun Dash	20	
			Numerical Methods and Algorithms	<i>Errors</i>	Tohida Rehman	2
				<i>Interpolation</i>		4
				<i>system of linear Equation</i>		10
				<i>Solution of Nonlinear Equation</i>		10
				<i>Solution of Differential Equations</i>		10
				<i>Integration</i>		4
				<i>Curve Fitting</i>		5
			Formal Languages and Automata Theory	Introduction to Formal Languages and Grammar	Sudeshna Tripathy	30
				Chomsky Classification		
	IV	Data Structures-II	<i>Trees</i>	Dhiman Karmakar	14	
			<i>Searching</i>		4	
			<i>Hashing</i>		4	
			<i>Sorting</i>		8	
		Programming through C Language	<i>Introduction</i>	Dhiman Karmakar	2	
			<i>C Programming elements</i>		2	
			<i>C Preprocessor</i>		6	
			<i>Statements</i>		2	
			<i>Functions</i>		6	
			<i>Array</i>		4	
			<i>Pointers</i>		4	
			<i>User defined Data types</i>		7	
			<i>File Access</i>		4	

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Lesson Plan of Part-III (Hons.)					
Subject	Paper	Topic	Sub-topic	Faculty	No. of Class
CMSA	V	Microprocessor and Computer Organization – II	Microprocessor	Himadri Bhattacharyya	50

			Evolution of Microprocessor		
			Case Studies		
		Computer Organization – II	ALU	Barun Dash	50
			Memory Hierarchy	Himadri Bhattacharyya	
			CISC and RISC processors	Barun Dash	
			Control Unit	Barun Dash	
			Computer Peripherals	Barun Dash	
		Computer Networks	Connections	Tohida Rehman	50
			transmission		
			Data Communication Concepts		
	VI	Object Oriented Programming	Concepts	Dhiman Karmakar	30
			Polymorphism		
			Namespace		
			Exception Handling		
		Software Engineering	Software Life Cycle	Sudeshna Tripathy	30
			Different Models		
			Testing		
		Computer Graphics	Introduction	Dhiman Karmakar	30
			Information Handling		
			Software Morphing		
			Computer Art		
			Projection & Clipping		
			2D & 3D Transformations		
		Data Base Management System	File Organization	Arpita Das Poddar	60
			Normal Forms		
			Basic concept		

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Lesson Plan of Part-I (GEN)					
Subject	Paper	Topic	Sub-topic	Faculty	No. of Class
CMSG	I	General Concepts	Information	Tohida Rehman	20
			Computer: Hardware		
			Software		
			Application Software		
			System Software Virus		
			Multimedia		
			Object Oriented Paradigm		
		Digital logic Design	Number System	Tayanika Mukherjee	35
			Combinational Logic		
			Sequential Logic		
		Computer Architecture	Memory	Dyuti Chatterjee	35

		and Organization			
			ALU		
			Control Unit		
			I/O		
			Central Processing Unit		
		Operating System	Operating System	Barun Dash	30
			Process scheduling		
			Memory Management		

Lesson Plan of Part-II (GEN)

CMSG	II	Algorithms & Data Structure	Flowchart	Barun Dash	45
			Data Structures		
			Sorting and Searching		
		Software Engineering	Models and Introduction to Analysis	Shukdeb Paul	25
			Requirement Specifications		
			Data Flow Diagram		
			Software life cycle models		
		Database Management System	Overview	Shukdeb Paul	50
			Data Models		
			Relational Model		
			Query Language		
			Relational Data Design		

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Lesson Plan of Part-III (GEN)

Subject	Paper	Topic	Sub-topic	Faculty	No. of Class
CMSG	IV	Communication and Computer Networks	Communication Concepts	Shukdeb Paul	60
			Transmission media		
			E-mail		
			LAN		
			Computer Networks	Himadri Bhattacharyya	
			The Internet		